Docket No. 030712-29 Serial No. 10/811,835 Page 1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Patent Application of:

Kazuaki GOTO et al.

Serial No. 10/811,835

Filed: 03/30/2004

For: METHOD OF DESIGNING A CIRCUIT
OF A SEMICONDUCTOR DEVICE
WITH A SPARE UNDERGROUND
CELL

OF A SEMICONDUCTOR DEVICE
WITH A SPARE UNDERGROUND
CELL

OF A SEMICONDUCTOR DEVICE
WITH A SPARE UNDERGROUND
CELL

CERTIFICATE OF MAILING OR TRANSMISSION [37 CFR 1.8(a)]

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage for First Class Mail in an envelope addressed to: Mail Stop ________, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, or being facsimile transmitted to the USPTO at 571 273 8300, on June 24 2007

Name: Jeannie Santon

AMENDMENT

Mail Stop
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated December 29, 2006, and in view of Applicant's Request for Continued Examination, filed concurrently herewith, please amend the above identified application as follows.